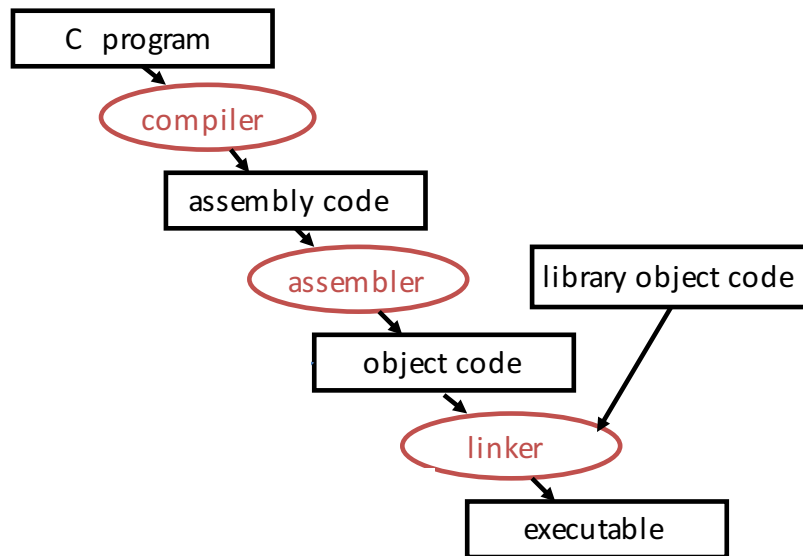
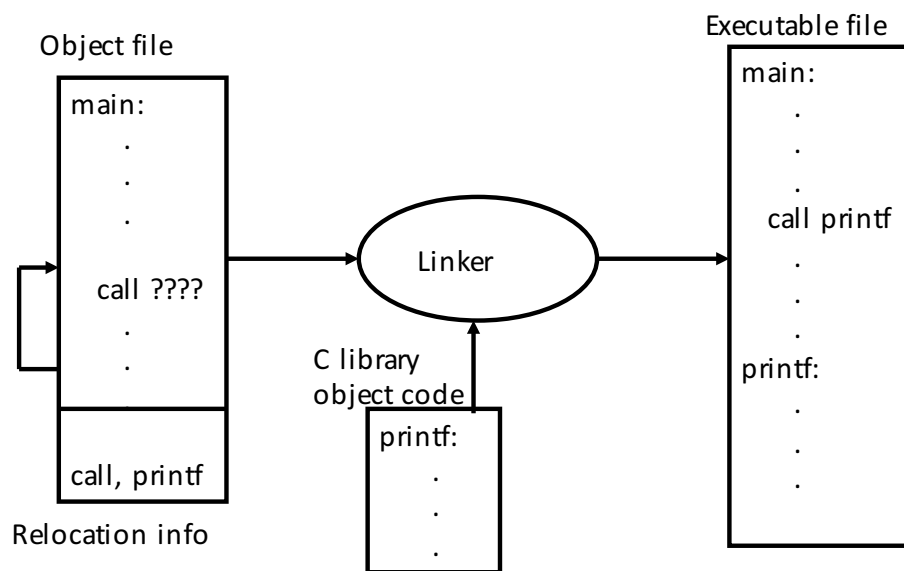


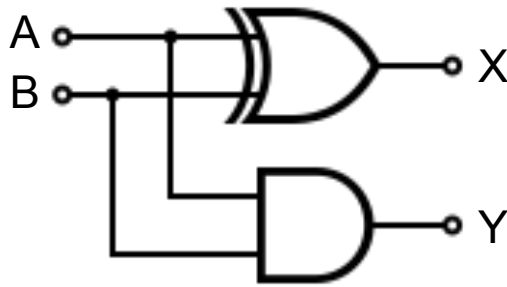
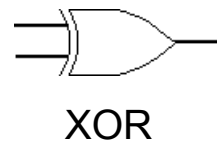
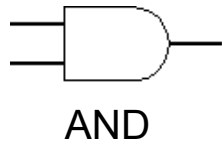
# Program Generation Workflow



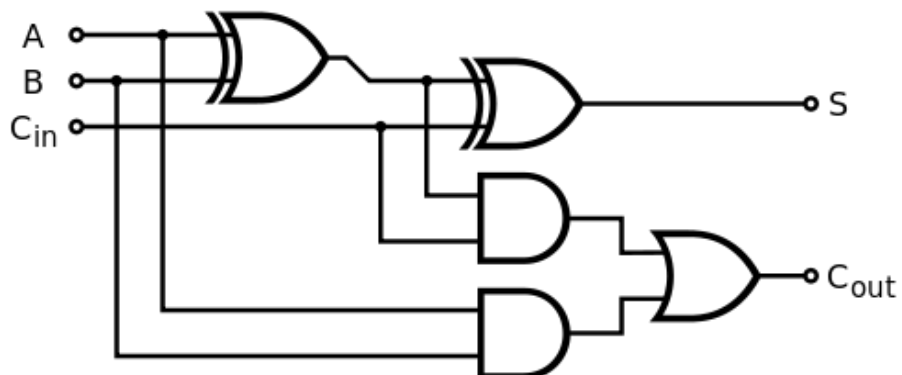
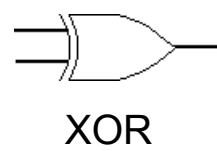
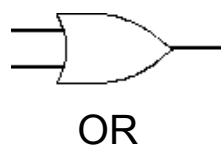
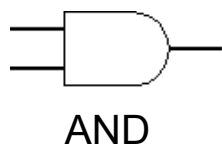
# Linking Schematic



# Circuits and Logic Gates

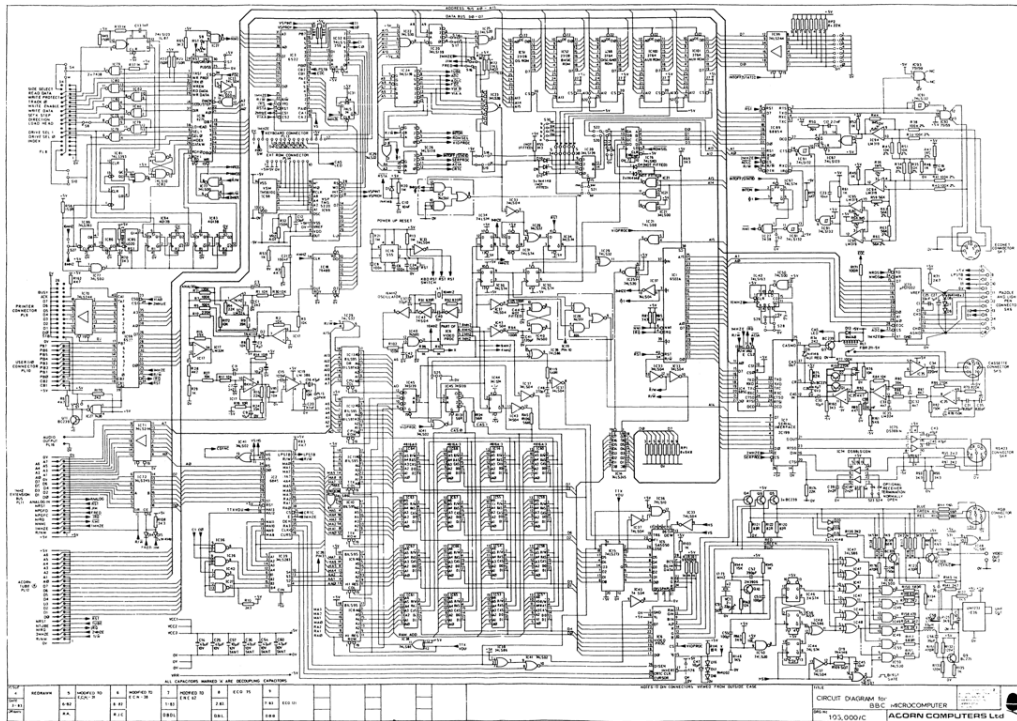


# Full Adder





# 30k Feet...



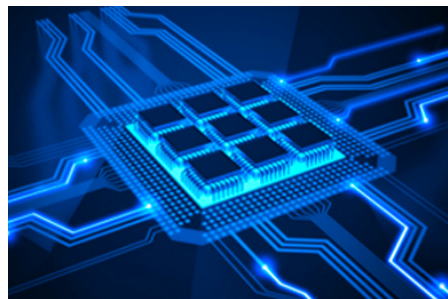
Bowdoin

Sean Barker

8

# Big Ideas in Systems

- Abstraction
- Representation
- Performance
- Security + Reliability



Bowdoin

Sean Barker

9