

2-Way Set Associative (1024 lines)

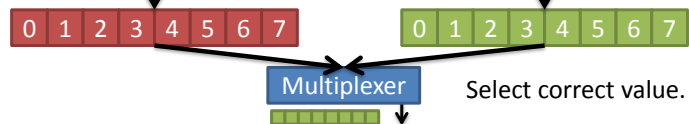
Tag (20 bits)	Set (9 bits)	Byte offset (3 bits)
3941	4	

Set #	Set 0				Set 1			
	V	D	Tag	Data (8 Bytes)	V	D	Tag	Data (8 Bytes)
0								
1								
2								
3								
4	1	1	4063		1	0	3941	
...			
508								
509								
510								
511								

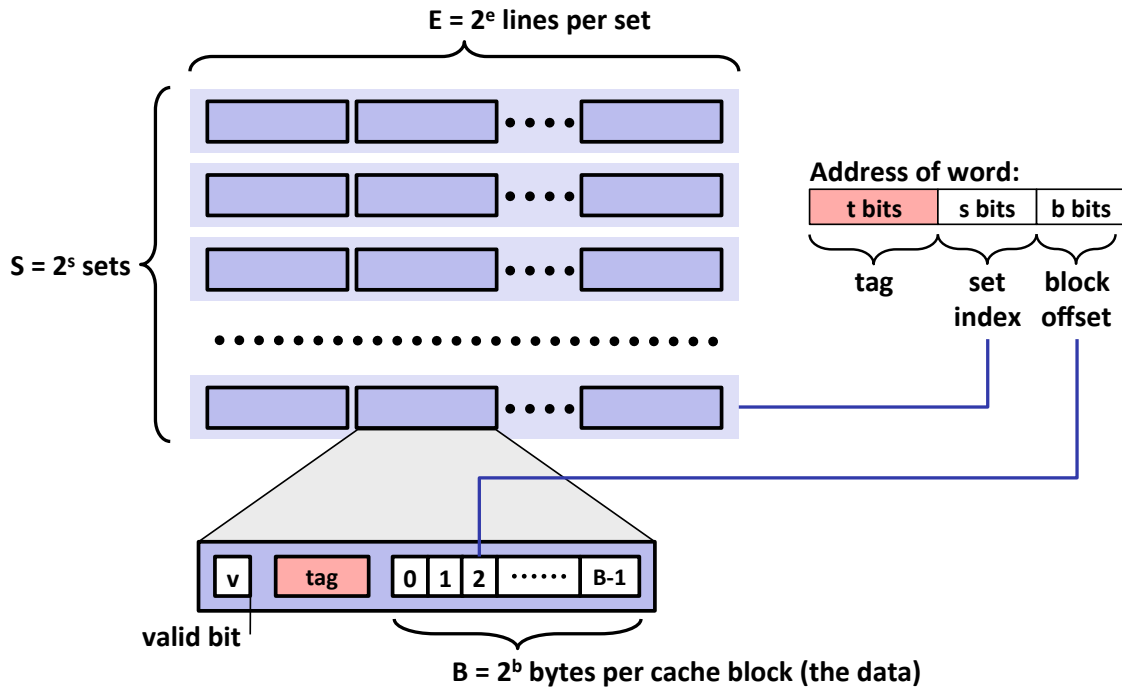
2-Way Set Associative Line Matching

Tag (20 bits)	Set (9 bits)	Byte offset (3 bits)
3941	4	

Set #	Set 0				Set 1			
	V	D	Tag	Data (8 Bytes)	V	D	Tag	Data (8 Bytes)
0								
1								
2								
3								
4	1	1	4063		1	0	3941	
...			
508								
509								
510								
511								



General Cache Model (S, E, B)



Intel Core i7 Cache Hierarchy

